

**SYSTEM AND METHOD FOR REDUCING CROSSTALK  
BETWEEN VIAS IN A PRINTED CIRCUIT BOARD**

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**ABSTRACT OF THE DISCLOSURE**

**[0054]** Two pairs of vias are arranged in a printed circuit board. A first pair of vias, which conveys a first signal pair, is arranged in a plane that is substantially equidistant from the vias in a second pair of vias, which conveys a second signal pair. Similarly, the second pair of vias is located in a plane that is substantially equidistant from each via in the first pair of vias. In some embodiments, such an arrangement reduces the crosstalk effect of the first signal pair on the second signal pair and vice versa.